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	Complete if Known				
	Application Number .	09/849822			
	Filing Date	05/04/01			
	First Named Inventor	G. Glenn Henry			
		Thomas C. McDonald			
	Group Art Unit	2183			
	Examiner Name	David Huisman			
_	Attorney Docket Number	CNTR.2050			

	U.S. PATENT DOCUMENTS							
Examiner	Cite No.	U.S. Patent Document		Name of Patentee or Applicant	Date of Publication	Pages, Columns, Lines, Where Relevant		
Initials		Number	Kind Code ^a	of Cited Document	of Cited Document MM-DD-YYYY	Passages or Relevant Figures Appear		
भिट्छ	AA	5142634		Fite et al	08/25/1992			
1001+	AB	5163140		Stiles et al	11/10/1992 .			
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DON'T	AK	6647467		Eric M. Dowling	11/11/2003			

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					Examiner Name	David Huisman	
	Sheet	2	of	2	Attorney Docket Number	CNTR.2050	

	OTHER PRIOR ART-NON PATENT LITERATURE DOCUMENTS						
Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²				
Ont	ВА	SAKAMOTO ET AL; Microarchitecture Support for Reducing Branch Penalty in a Superscaler Processor; pp. 208-216; IEEE 1996; Mitsubishi Electric Corp., System LSI Laboratory, 4-1 Mizuhara, Itami, Hyogo 664, Japan.					
Dort	ВВ	YEH ET AL; <u>ALTERNATIVE IMPLEMENTATION OF TWO-LEVEL ADAPTIVE BRANCH PREDICTION;</u> 19 TH Annual International Symposium on Computer Architecture, pp. 124-134, May 19-21, 1992, Gold Coast, Australia.					
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ont	BE	IEEE 100; THE AUTHORITATIVE DICTIONARY OF IEEE STANDARDS TERMS; Seventh Edition, IEEE, Standards Information Network, IEEE Press,					
hto	BF	BRAY ET AL; STRATEGIES FOR BRANCH TARGET BUFFERS; Technical Report No. CSL-TR-91-480, June 1991.					
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